

ISSUE CLASSIFICATION	
Class	Subclass

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

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U.S. UTILITY Patent Application

O.J.P.E.

PATENT DATE

SCANNED 1763 Q.A. AG

APPLICATION NO. 09/926531	CONT/PRIOR D F	CLASS 174	SUBCLASS	ART UNIT 2831	EXAMINER
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[illegible]

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Vertical electrical interconnections in a stack

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		NOTICE OF ALLOWANCE MAILED	
			<div style="text-align: center;"> ISSUE FEE </div> <div> <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;">Amount Due</div> <div style="width: 45%;">Date Paid</div> </div> </div>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <div style="text-align: center;">_____</div> <div style="text-align: center;">_____</div>	_____ (Primary Examiner) _____ (Date)			
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